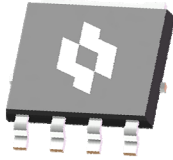
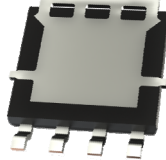


**Product Summary**

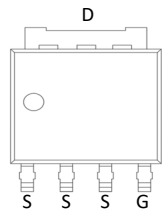
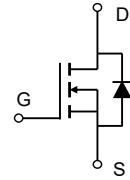
| $V_{DS}$ | $R_{DS(ON)_TYP}$         | $I_{D\_MAX}$ |
|----------|--------------------------|--------------|
| 45 V     | 0.95 mΩ @ $V_{GS} = 10V$ | 265 A        |

**LFPAK5060**


Top View



Bottom View


PIN Configuration  
(Top View)


Schematic Diagram

**Features**

- Low On-Resistance
- Excellent FoM (figure of merit)
- 100% ΔVDS & UIS & Rg Tested


**Applications**

- Load switching
- Motor drives
- High frequency switching, synchronous rectification

**Mechanical Data**

- Green molding compound
- Moisture sensitivity: level 1 per J-STD-020
- UL flammability classification rating 94V-0

**Ordering Information**

| Orderable Part Number | Package Type | Device Marking | Form          | Quantity (pcs) |
|-----------------------|--------------|----------------|---------------|----------------|
| SMT4501CHFP5          | LFPAK5060    | 4501CH         | 13" Tape&Reel | 5,000          |

**Maximum Ratings** (@  $T_C = 25^\circ\text{C}$ , unless otherwise specified.)

| Parameter                                                  | Symbol         | Value      | Unit |
|------------------------------------------------------------|----------------|------------|------|
| Drain - Source Voltage                                     | $V_{DS}$       | 45         | V    |
| Gate - Source Voltage                                      | $V_{GS}$       | ±20        | V    |
| Continuous Drain Current ( $V_{GS} = 10V$ ) <sup>(1)</sup> | $I_D$          | 265        | A    |
|                                                            |                | 167        | A    |
| Pulsed Drain Current <sup>(2)</sup>                        | $I_{DM}$       | 1058       | A    |
| Single Pulse Avalanche Energy <sup>(3)</sup>               | $E_{AS}$       | 672        | mJ   |
| Single Pulse Avalanche Current ( $L = 0.1mH$ )             | $I_{AS}$       | 66         | A    |
| Power Dissipation                                          | $P_D$          | 128        | W    |
|                                                            |                | 51         | W    |
| Junction & Storage Temperature Range                       | $T_J, T_{STG}$ | -55 ~ +150 | °C   |

**Thermal Characteristics**

| Parameter                                              | Symbol          | Typ. | Max. | Unit |
|--------------------------------------------------------|-----------------|------|------|------|
| Thermal Resistance, Junction-to-Ambient <sup>(4)</sup> | $R_{\theta JA}$ | 32   | 40   | °C/W |
| Thermal Resistance, Junction-to-Case <sup>(5)</sup>    | $R_{\theta JC}$ | 0.75 | 0.98 | °C/W |

**Electrical Characteristics** (@  $T_J = 25^\circ\text{C}$ , unless otherwise specified.)

| Parameter                                         | Symbol               | Test Condition                                                                                | Min. | Typ. | Max. | Unit |
|---------------------------------------------------|----------------------|-----------------------------------------------------------------------------------------------|------|------|------|------|
| Off Characteristics <sup>(6)</sup>                |                      |                                                                                               |      |      |      |      |
| Drain-Source Breakdown Voltage                    | V <sub>(BR)DSS</sub> | V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA                                                  | 45   | -    | -    | V    |
| Zero Gate Voltage Drain Current                   | I <sub>DSS</sub>     | V <sub>DS</sub> = 45V, V <sub>GS</sub> = 0V                                                   | -    | -    | 1.0  | μA   |
|                                                   |                      | T <sub>J</sub> = 125°C                                                                        | -    | -    | 100  | μA   |
| Gate-Source Leakage Current                       | I <sub>GSS</sub>     | V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V                                                  | -    | -    | ±100 | nA   |
| On Characteristics <sup>(6)</sup>                 |                      |                                                                                               |      |      |      |      |
| Gate Threshold Voltage                            | V <sub>GS(th)</sub>  | V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 250μA                                    | 2.0  | 3.0  | 4.0  | V    |
| Static Drain-Source On-Resistance                 | R <sub>DS(ON)</sub>  | V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A                                                   | -    | 0.95 | 1.15 | mΩ   |
| Forward Transconductance                          | g <sub>fs</sub>      | V <sub>DS</sub> = 5.0V, I <sub>D</sub> = 20A                                                  | -    | 57   | -    | S    |
| Diodes Forward Voltage                            | V <sub>SD</sub>      | I <sub>S</sub> = 2.0A, V <sub>GS</sub> = 0V                                                   | -    | 0.7  | 1.2  | V    |
| Dynamic Characteristics <sup>(7)</sup>            |                      |                                                                                               |      |      |      |      |
| Input Capacitance                                 | C <sub>iss</sub>     | V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V, f = 1MHz                                         | -    | 3936 | -    | pF   |
| Output Capacitance                                | C <sub>oss</sub>     |                                                                                               | -    | 2312 | -    | pF   |
| Reverse Transfer Capacitance                      | C <sub>rss</sub>     |                                                                                               | -    | 93   | -    | pF   |
| Gate Resistance                                   | R <sub>g</sub>       | V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1MHz                                          | -    | 1.8  | -    | Ω    |
| Switching Characteristics <sup>(7)</sup>          |                      |                                                                                               |      |      |      |      |
| Turn-On DelayTime                                 | t <sub>d(on)</sub>   | V <sub>GS</sub> = 10V, V <sub>DS</sub> = 20V<br>I <sub>D</sub> = 20A, R <sub>GEN</sub> = 3.0Ω | -    | 8.2  | -    | ns   |
| Rise Time                                         | t <sub>r</sub>       |                                                                                               | -    | 18   | -    | ns   |
| Turn-Off DelayTime                                | t <sub>d(off)</sub>  |                                                                                               | -    | 29   | -    | ns   |
| Fall Time                                         | t <sub>f</sub>       |                                                                                               | -    | 17   | -    | ns   |
| Gate Charge Characteristics <sup>(7)</sup>        |                      |                                                                                               |      |      |      |      |
| Total Gate Charge (V <sub>GS</sub> = 10V)         | Q <sub>g</sub>       | V <sub>DS</sub> = 20V, I <sub>D</sub> = 20A<br>V <sub>GS</sub> = 10V                          | -    | 52   | -    | nC   |
| Total Gate Charge (V <sub>GS</sub> = 7.0V)        | Q <sub>g</sub>       |                                                                                               | -    | 37   | -    | nC   |
| Gate-Source Charge                                | Q <sub>gs</sub>      |                                                                                               | -    | 15.9 | -    | nC   |
| Gate-Drain Charge                                 | Q <sub>gd</sub>      |                                                                                               | -    | 9.3  | -    | nC   |
| Gate Plateau Voltage                              | V <sub>plateau</sub> |                                                                                               | -    | 4.5  | -    | V    |
| Drain-Source Diode Characteristics <sup>(7)</sup> |                      |                                                                                               |      |      |      |      |
| Body Diode Reverse Recovery Time                  | t <sub>rr</sub>      | I <sub>F</sub> = 20A, dI/dt = 100A/μs,<br>T <sub>J</sub> = 25°C                               | -    | 59   | -    | ns   |
| Body Diode Reverse Recovery Charge                | Q <sub>rr</sub>      |                                                                                               | -    | 70   | -    | nC   |
| Diode Forward Current                             | I <sub>S</sub>       | T <sub>C</sub> = 25°C                                                                         | -    | -    | 181  | A    |

**Notes:**

1. This current is chip limited, which is calculated based on  $R_{thjc}$ .
2. This current is calculated on single pulse with 10 $\mu s$  Sinle Pulse.
3. Defined by design, not subject to production test,  $E_{AS}$  condition:  $T_J=25^\circ\text{C}$ ,  $V_{DD}=20V$ ,  $V_{GS}=10V$ ,  $L=1.0mH$ .
4. Device mounted on FR-4 substrate PC board with 2oz copper in 1inch square cooling area.
5. Thermal resistance from junction to the exposed pad.
6. Short duration pulse test used to minimize self-heating effect.
7. Defined by design, not subject to production.

### Typical Electrical and Thermal Characteristics

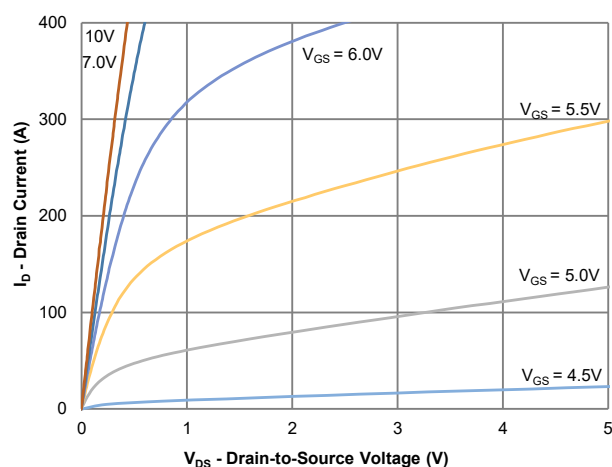


Figure 1: Output Characteristics

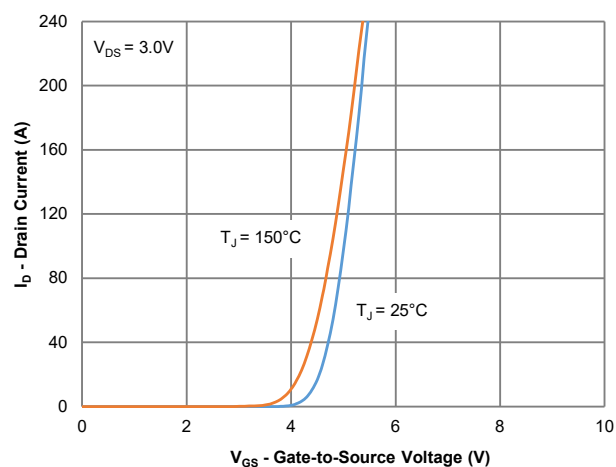


Figure 2: Transfer Characteristics

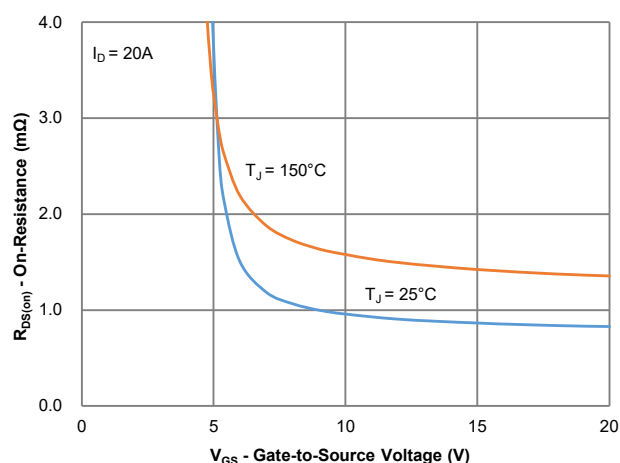


Figure 3: On-Resistance vs. Gate-Source Voltage

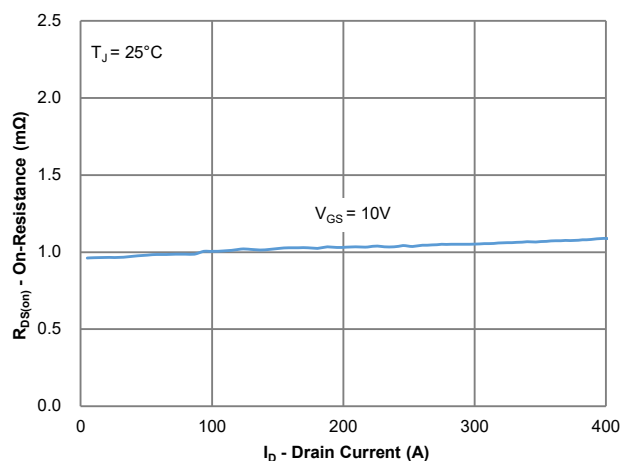


Figure 4: On-Resistance vs. Drain Current and Gate Voltage

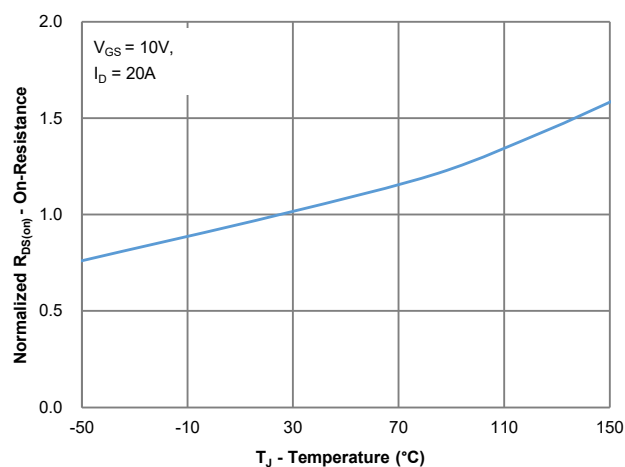


Figure 5: On-Resistance vs. Junction Temperature

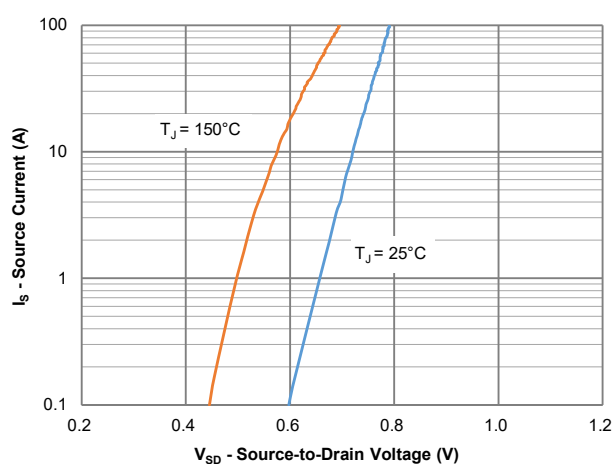


Figure 6: Source-Drain Diode Forward Voltage

### Typical Electrical and Thermal Characteristics

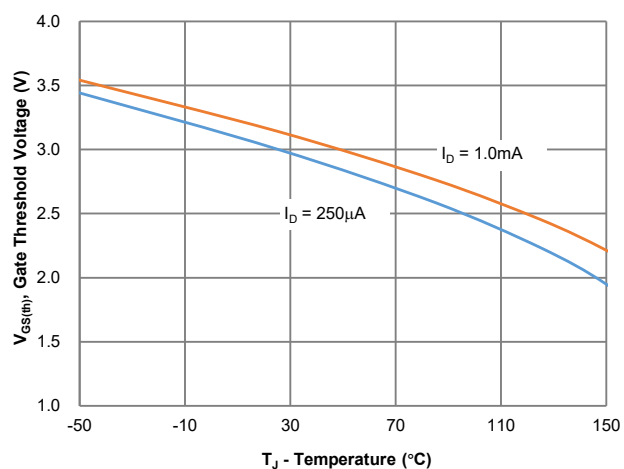


Figure 7: Gate Threshold Variation vs. Junction Temperature

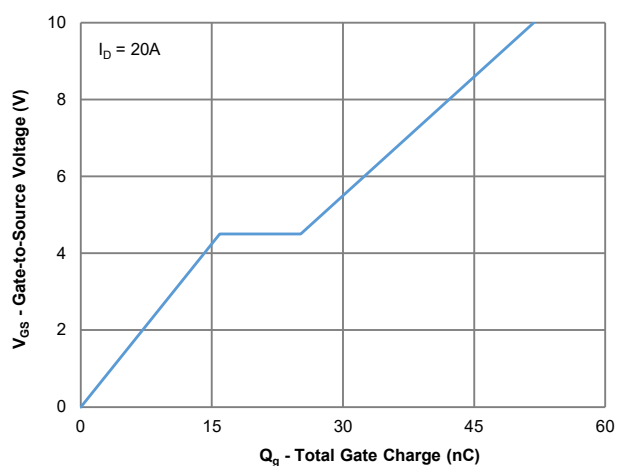


Figure 8: Gate Charge Characteristics

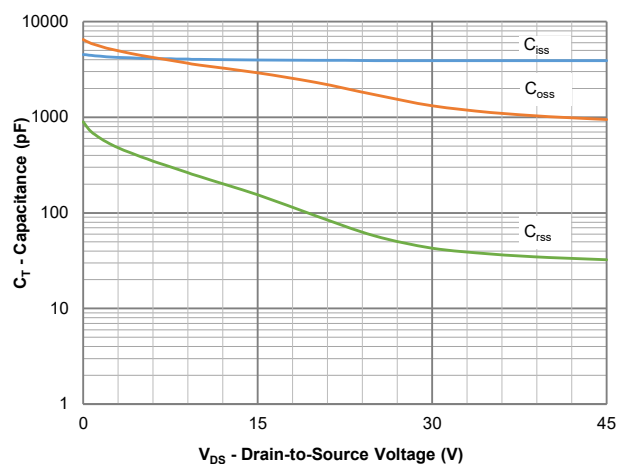


Figure 9: Capacitance Characteristics

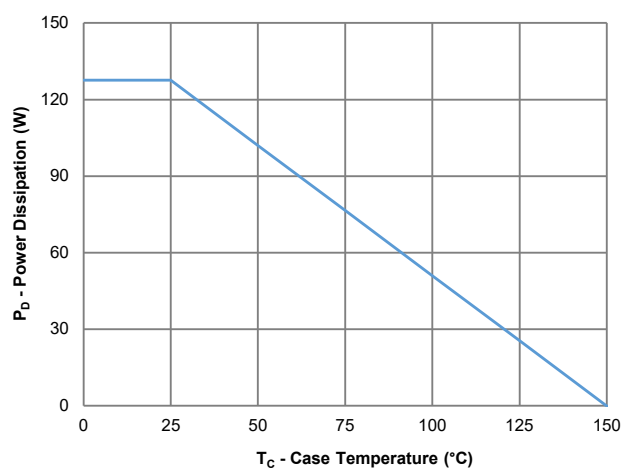


Figure 10: Power Derating

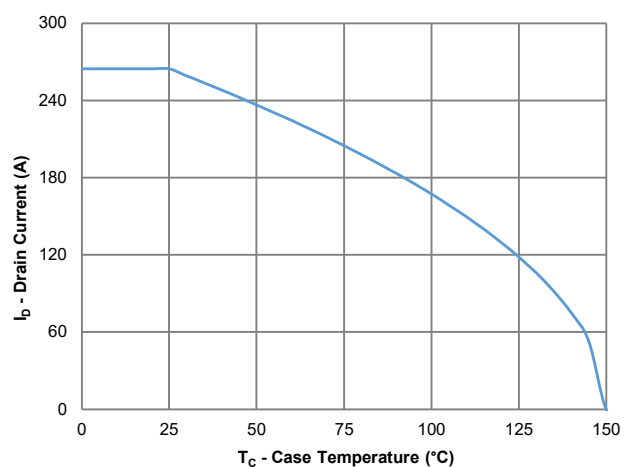


Figure 11: Current Derating

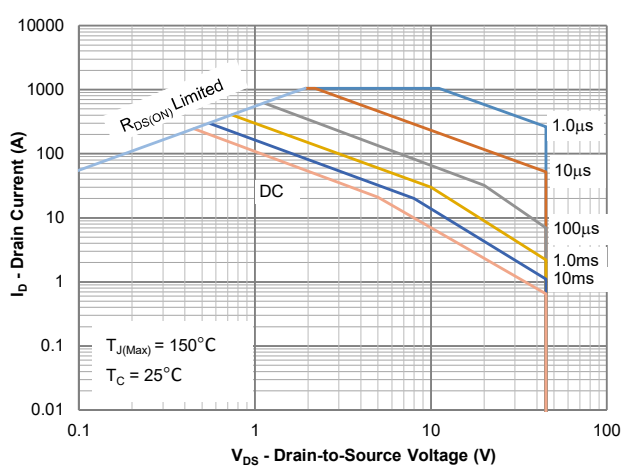
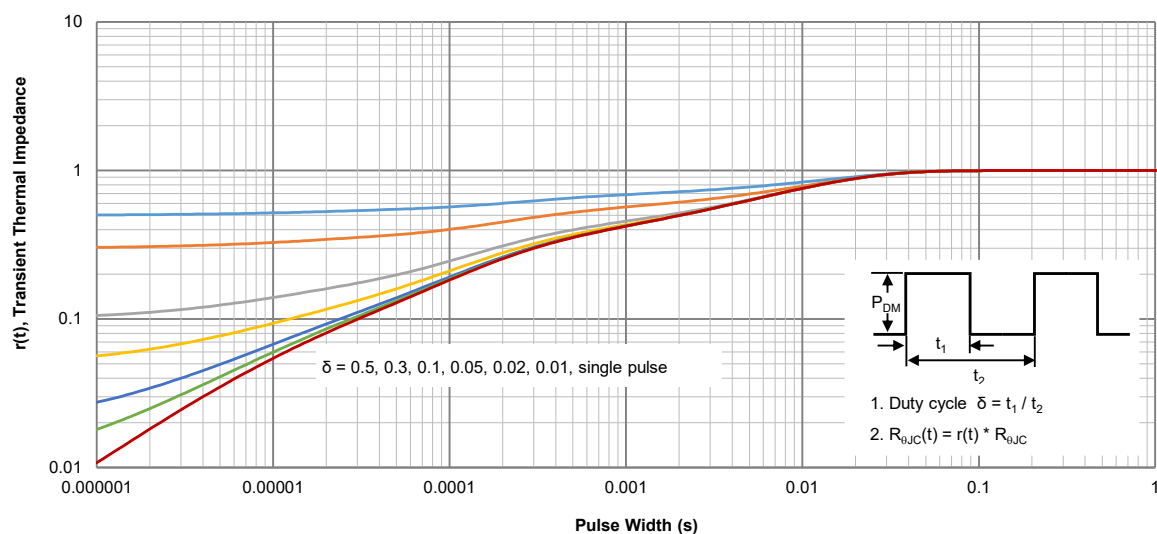
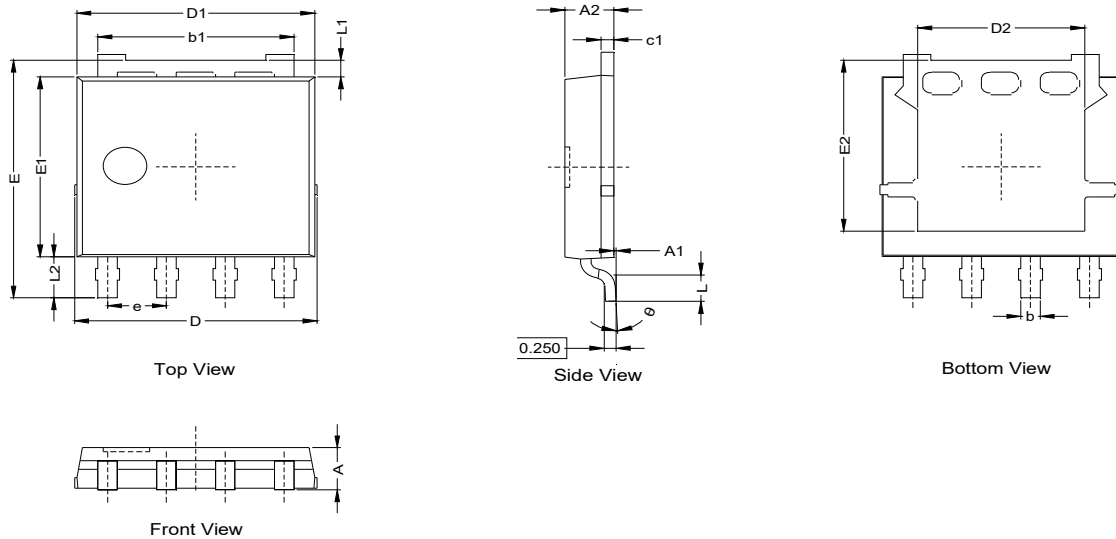


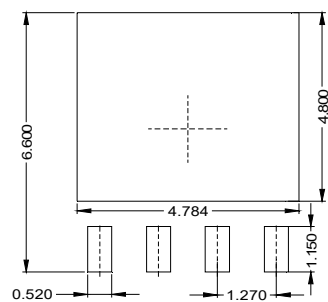
Figure 12: Safe Operating Area

**Typical Electrical and Thermal Characteristics**

**Figure 13: Normalized Maximum Transient Thermal Impedance**

**LFPAK5060 Package Outline**
**Package Outline**

**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M,1994.
2. ALL DIMNESIONS IN MILLIMETER (ANNGLE IN DEGREE).
3. DIMENSIONS "D1" AND "E1" DO NOT INCLUDE MOLD FLASH PROTRUSIONS OR GATE BURRS.

| DIM. | MILLIMETER |      |      |
|------|------------|------|------|
|      | MIN.       | NOM. | MAX. |
| A    | 1.00       | --   | 1.30 |
| A1   | 0.00       | --   | 0.15 |
| A2   | 0.98       | 1.03 | 1.10 |
| b    | 0.35       | 0.42 | 0.50 |
| b1   | 4.02       | 4.23 | 4.41 |
| c    | 0.19       | 0.22 | 0.25 |
| c1   | 0.24       | 0.27 | 0.30 |
| D    | 5.10       | --   | 5.30 |
| D1   | 4.95       | 5.13 | 5.30 |
| D2   | 3.50       | --   | 3.70 |
| E    | 5.95       | --   | 6.20 |
| E1   | 4.45       | 4.58 | 4.70 |
| E2   | 4.10       | --   | 4.45 |
| e    | 1.27 BSC.  |      |      |
| L    | 0.40       | 0.65 | 0.85 |
| L1   | 0.27       | --   | 0.57 |
| L2   | 0.80       | --   | 1.30 |
| θ    | 0°         | --   | 8°   |

**Recommended Soldering Footprint**


DIMENSIONS: MILLIMETERS