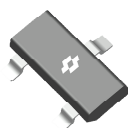
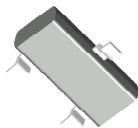


Product Summary

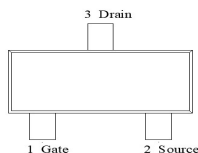
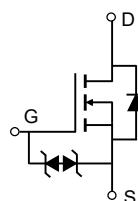
V_{DS}	$R_{DS(ON)_TYP}$	I_{D_MAX}
100 V	112 m Ω @ $V_{GS} = 10V$	2.0 A
	140 m Ω @ $V_{GS} = 4.5V$	

SOT-23


Top View



Bottom View


PIN Configuration
(Top View)


Schematic Diagram

Features

- Low On-Resistance
- Low Gate Charge
- High Speed Power Switching
- ESD Protected up to 2.0KV(HBM)


**HALOGEN
FREE**
Applications

- Motor controls
- DC/DC in Telecoms and Industrial
- High frequency switching, synchronous rectification

Mechanical Data

- Green Molding Compound
- UL Flammability Classification Rating 94V-0

Ordering Information

Orderable Part Number	Package Type	Device Marking	Form	Quantity (pcs)
SMT10T130ALMK	SOT-23	A130LK	7" Tape&Reel	3,000

Maximum Ratings (@ $T_A = 25^\circ\text{C}$, unless otherwise specified.)

Parameter	Symbol	Value	Unit
Drain - Source Voltage	V_{DS}	100	V
Gate - Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($V_{GS} = 10V$) ⁽¹⁾	I_D	2.0	A
		1.6	A
Pulsed Drain Current ⁽²⁾	I_{DM}	7.9	A
Power Dissipation	P_D	0.96	W
		0.62	W
Junction & Storage Temperature Range	T_J, T_{STG}	-55 ~ +150	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Typ.	Max.	Unit
Thermal Resistance, Junction-to-Ambient ⁽³⁾	$R_{\theta JA}$	100	130	$^\circ\text{C/W}$

Electrical Characteristics (@ $T_J = 25^\circ\text{C}$, unless otherwise specified.)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Off Characteristics ⁽⁴⁾						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	100	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100V, V _{GS} = 0V	-	-	1.0	μA
		T _J = 125°C	-	-	100	μA
Gate-Source Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V	-	-	±10	μA
On Characteristics ⁽⁴⁾						
Gate Threshold Voltage	V _{GS(th)}	V _{GS} = V _{DS} , I _D = 250μA	1.2	1.8	2.5	V
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} = 10V, I _D = 2.0A	-	112	135	mΩ
		V _{GS} = 4.5V, I _D = 1.0A	-	140	175	mΩ
Forward Transconductance	g _{fs}	V _{DS} = 5.0V, I _D = 2.0A	-	4.8	-	S
Diodes Forward Voltage	V _{SD}	I _S = 2.0A, V _{GS} = 0V	-	0.86	1.2	V
Dynamic Characteristics ⁽⁵⁾						
Input Capacitance	C _{iss}	V _{DS} = 50V, V _{GS} = 0V, f = 1MHz	-	56	-	pF
Output Capacitance	C _{oss}		-	40	-	pF
Reverse Transfer Capacitance	C _{rss}		-	5.3	-	pF
Gate Resistance	R _g	V _{GS} = 0V, V _{DS} = 0V, f = 1MHz	-	44	-	Ω
Switching Characteristics ⁽⁵⁾						
Turn-On DelayTime	t _{d(on)}	V _{GS} = 10V, V _{DS} = 50V I _D = 2.0A, R _{GEN} = 3.0Ω	-	0.92	-	ns
Rise Time	t _r		-	2.7	-	ns
Turn-Off DelayTime	t _{d(off)}		-	5.3	-	ns
Fall Time	t _f		-	2.0	-	ns
Gate Charge Characteristics ⁽⁵⁾						
Total Gate Charge (V _{GS} = 10V)	Q _g	V _{DS} = 50V, I _D = 2.0A V _{GS} = 10V	-	2.3	-	nC
Total Gate Charge (V _{GS} = 4.5V)	Q _g		-	1.3	-	nC
Gate-Source Charge	Q _{gs}		-	0.33	-	nC
Gate-Drain Charge	Q _{gd}		-	0.67	-	nC
Gate Plateau Voltage	V _{plateau}		-	3.0	-	V
Drain-Source Diode Characteristics ⁽⁵⁾						
Body Diode Reverse Recovery Time	t _{rr}	I _F = 2.0A, dI/dt = 100A/μs,	-	21	-	ns
Body Diode Reverse Recovery Charge	Q _{rr}	T _J = 25°C	-	14	-	nC
Diode Forward Current	I _S	T _A = 25°C	-	-	2.0	A

Notes:

1. This current is chip limited, which is calculated based on R_{thjc} .
2. This current is calculated on single pulse with $10\mu s$ Pulse & Duty Cycle = 1%.
3. Device mounted on FR-4 substrate PC board with 2oz copper in 1inch square cooling area.
6. Short duration pulse test used to minimize self-heating effect.
5. Defined by design, not subject to production.

Typical Electrical and Thermal Characteristics

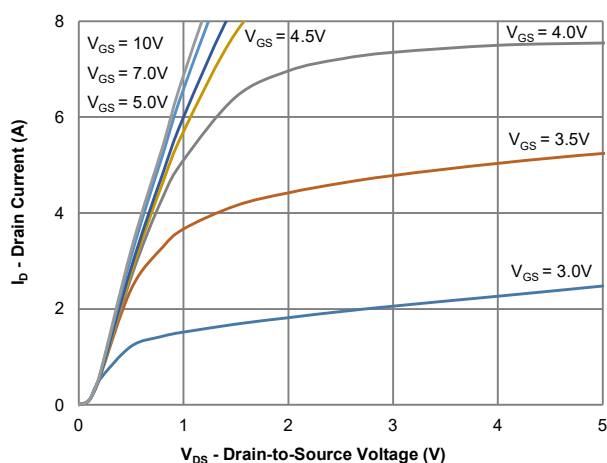


Figure 1: Output Characteristics

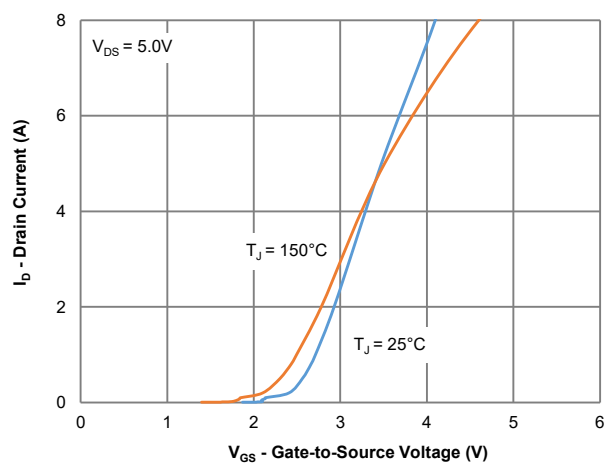


Figure 2: Transfer Characteristics

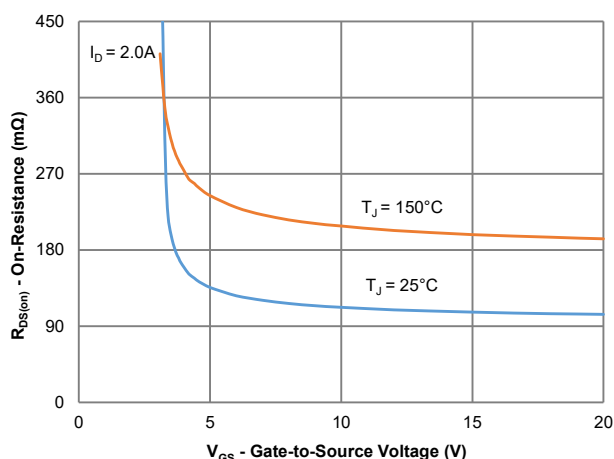


Figure 3: On-Resistance vs. Gate-Source Voltage

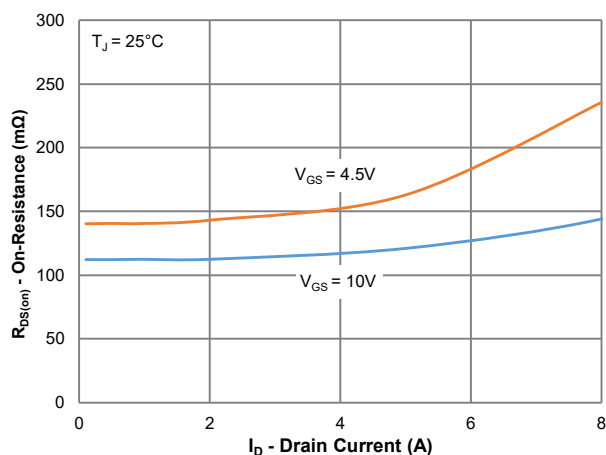


Figure 4: On-Resistance vs. Drain Current and Gate Voltage

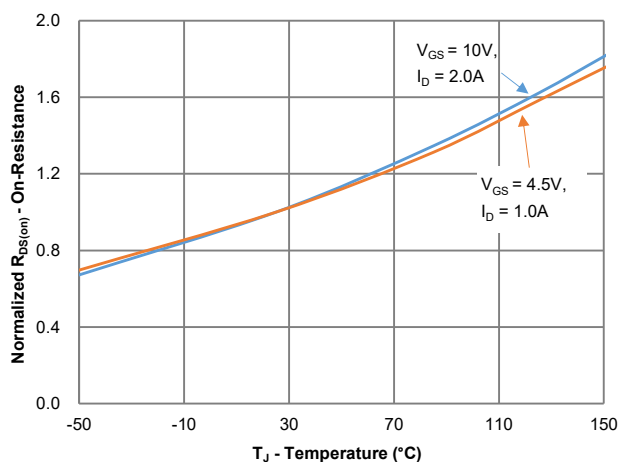


Figure 5: On-Resistance vs. Junction Temperature

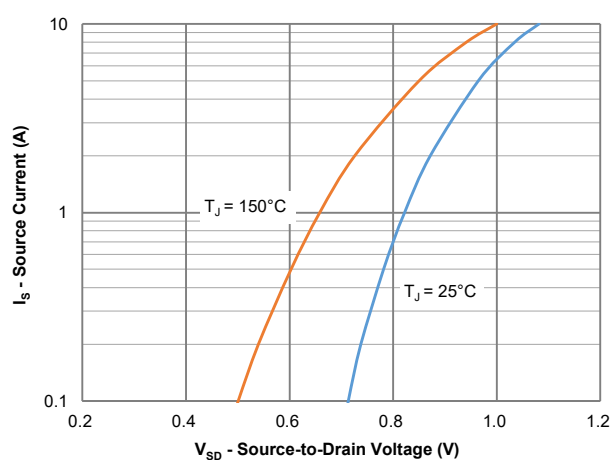
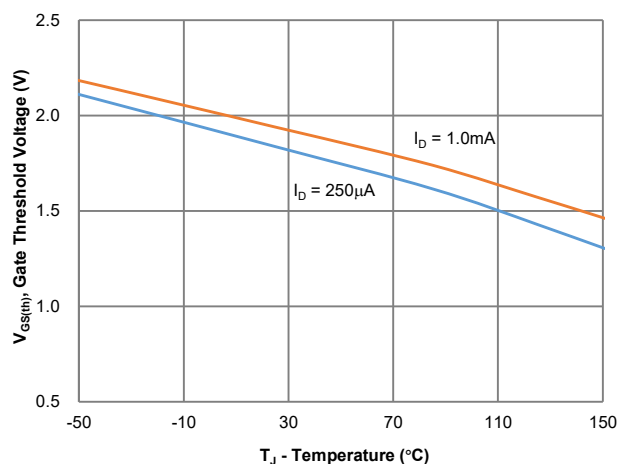
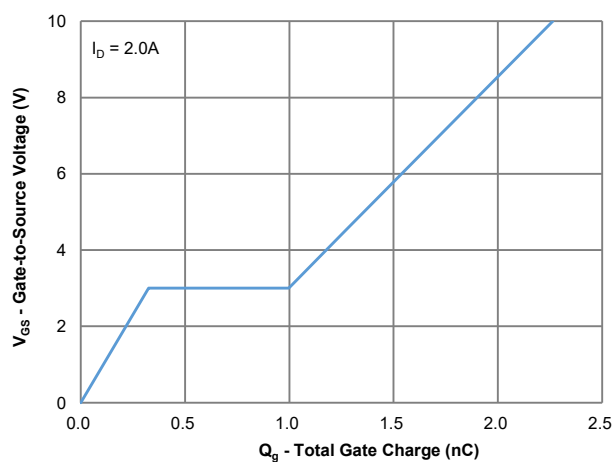
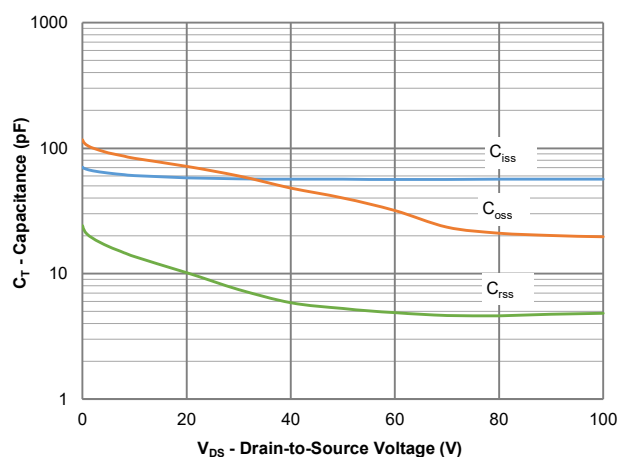
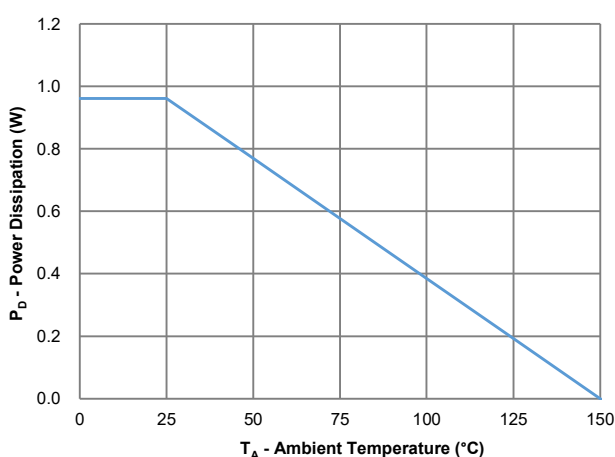
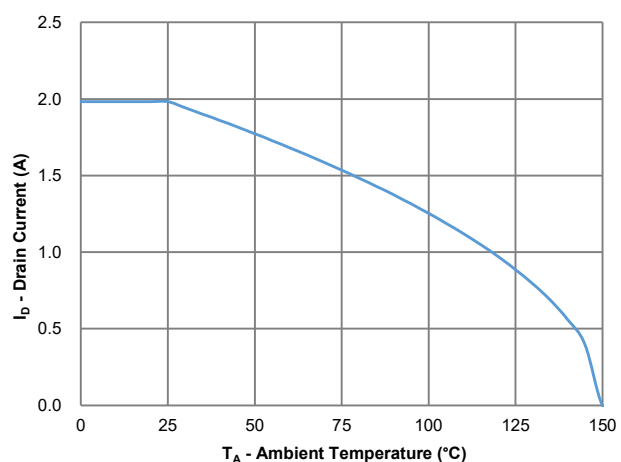
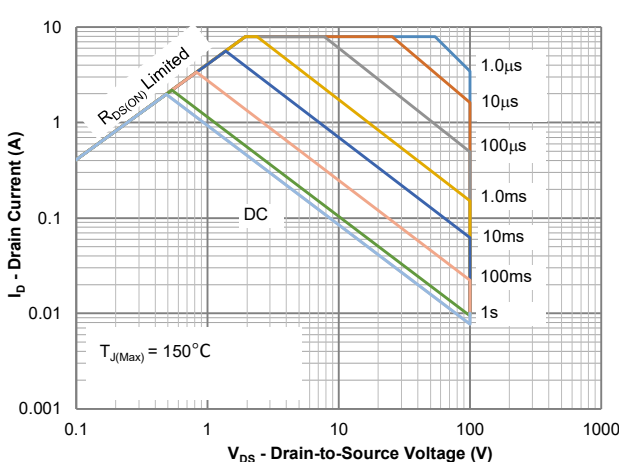
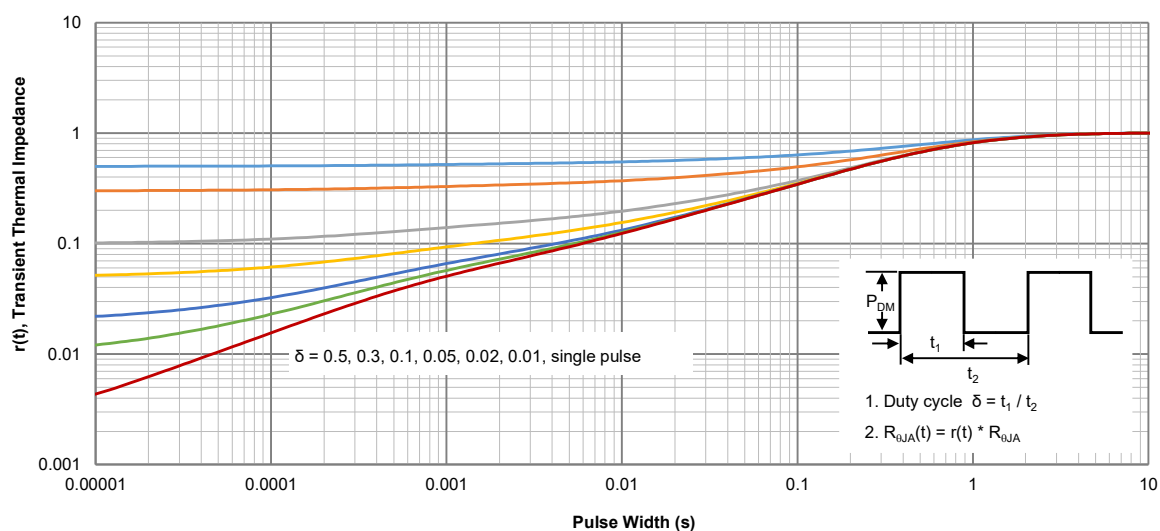
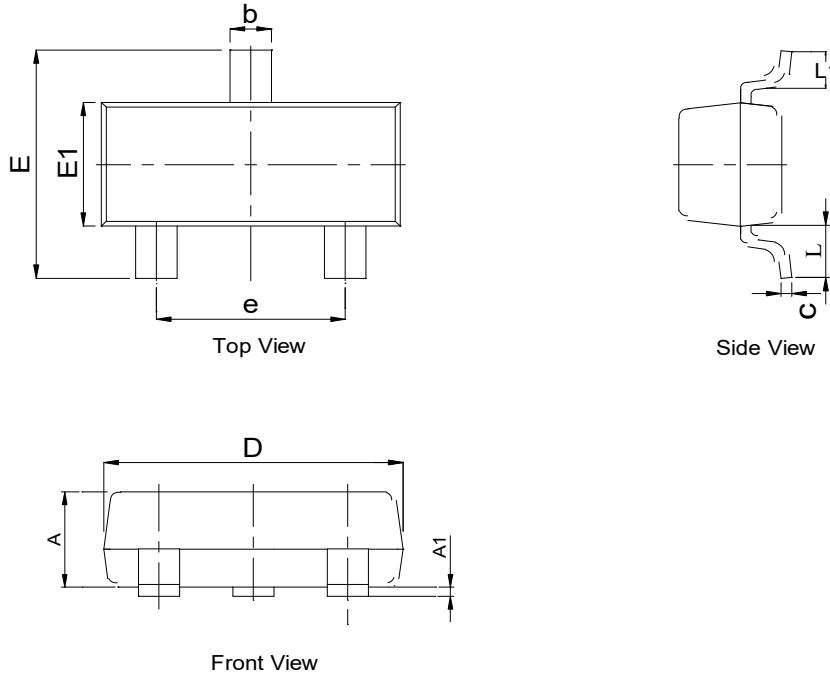


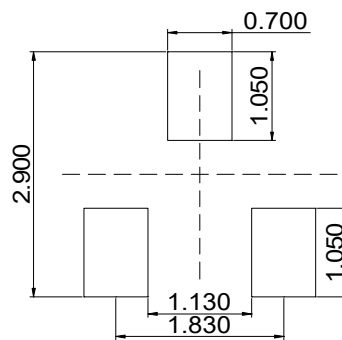
Figure 6: Source-Drain Diode Forward Voltage

Typical Electrical and Thermal Characteristics

Figure 7: Gate Threshold Variation vs. Junction Temperature

Figure 8: Gate Charge Characteristics

Figure 9: Capacitance Characteristics

Figure 10: Power Derating

Figure 11: Current Derating

Figure 12: Safe Operating Area

Typical Electrical and Thermal Characteristics

Figure 13: Normalized Maximum Transient Thermal Impedance

SOT-23 Package Outline
Package Outline


DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
A1	0.00	-	0.10
D	2.80	2.90	3.00
E	2.30	2.40	2.50
E1	1.20	1.30	1.40
L	0.45	0.55	0.65
L1	0.20	-	0.55
b	0.30	0.40	0.50
c	-	0.10	0.15
e	-	1.90	-

Recommended Soldering Footprint


DIMENSIONS: MILLIMETERS